

JAN 26 2006

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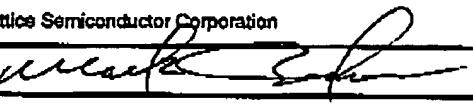
Total Number of Pages in This Submission

Application Number	10/726,972
Filing Date	12/03/2003
First Named Inventor	Jack F Tomlinson
Art Unit	2116
Examiner Name	Suresh Suryawanshi
Total Number of Pages in This Submission	9
Attorney Docket Number	L00-010C1

ENCLOSURES (Check all that apply)

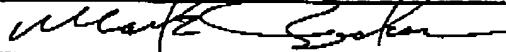
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Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Lattice Semiconductor Corporation		
Signature			
Printed name	Mark L. Becker		
Date	1/26/06	Reg. No.	31325

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

First Named Inventor: Jock F Tomlinson

Application No.: 10/726,972

Filed: 12/03/2003

For PROGRAMMABLE POWER MANAGEMENT SYSTEM
AND METHOD

Examiner: Suresh Suryawanshi

Art Unit: 2115

Attorney Docket No. L00-010C1

RESPONSE

Applicants respond to the Office action mailed 11/25/2005 as follows.